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/GP/	Sheu-Chih Cheng et al., "A Comparison of Block-Matching Algorithms Mapped to Systolic-Array Implementation", IEEE Transactions on Circuits and Systems for Video Technology, Vol. 7, No. 5, October 1997, pps. 741-757.														
/GP/	Eiji Ogura et al., "A 1.2-W Single-Chip MPEG2 MP@ML Video Encoder LSI Including Wide Search Range (H: $\pm 288$ , V: $\pm 96$ ) Motion Estimation and 81-MOPS Controller", IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, November 1998, pps. 1765-1771.														
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